



- ☐ Drafts
- ☐ Pending
- ☒ Active
 - ☒ L1: (4778) power-on adj2 reset\$4
 - ☒ L2: (1) power adj on adj2 reset\$4
 - ☒ L4: (1868) power-up adj2 reset\$4
 - ☒ L5: (101) 1 same flash adj2 memory
 - ☒ L8: (0) 6 same latch\$4
 - ☒ L7: (25) 5 same latch\$4
 - ☒ L9: (76) 5 not 7
 - ☒ L6: (63) 4 same flash adj2 memory
 - ☒ L17: (1256) power-on-reset\$4
 - ☒ L18: (7) 17 same flash adj2 memory
 - ☒ L20: (102) 17 and flash adj2 memory
 - ☒ L21: (495) 1 and flash adj2 memory
 - ☒ L22: (249) 4 and flash adj2 memory
 - ☒ L23: (1355) 327/143
 - ☒ L24: (1180) 327/198
 - ☒ L25: (2195) 365/185.33
 - ☒ L26: (848) 365/185.2
 - ☒ L27: (6490) 1 or 4
 - ☒ L28: (7450) 1 or 4 or 17
 - ☒ L29: (372) 28 and 23
 - ☒ L30: (182) 28 and 24
 - ☒ L32: (10) 28 and 26
 - ☒ L31: (54) 28 and 25
 - ☒ L33: (7) (shao\$3 near2 chou).in.
- ☐ Failed
- ☒ Saved
- ☐ Favorites

DBs: USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

Default operator: OR

☒ Burels☒ Highlight all hit terms initially

	U	1	2	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cla	Inventor	S	C	P	3	S	Im
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5784314 A	19980721	7	Method for setting the threshold voltage of a reference memory cell	365/185.2	365/185.24		Sali, Mauro et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5936443 A	19990810	21	Power-on reset signal generator for semiconductor device	327/143	327/198		Yasuda, Kenichi et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6278634 B1	20010821	16	Reference memory cell initialization circuit and method	365/185.2	365/185.18; 365/185.28		Ra, Kyeong Man	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
4	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6317362 B1	20011113	15	Semiconductor memory device	365/185.2	365/185.21		Nomura, Hidemi et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
5	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6737884 B2	20040518	18	Power-on reset circuit and IC card	328/38	327/143		Shigemasa, Haruhiko et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US

Ready

2:58 PM